

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Seo et al.

Serial No. To be assigned

Filed: Concurrently herewith

For: **METHODS OF FORMING A CONDUCTIVE STRUCTURE IN AN INTEGRATED  
CIRCUIT DEVICE**

Date: March 9, 2004

MAIL STOP PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

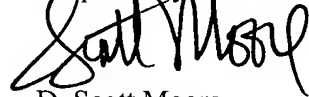
**INFORMATION DISCLOSURE STATEMENT**

Sir:

Attached is a list of documents on Form PTO-1449, together with a copy of any listed foreign patent document and/or non-patent literature. A copy of any listed U.S. patent and/or U.S. patent application publication is not provided herewith in accordance with the waiver by the U.S. Patent and Trademark Office of requirements under 37 C.F.R. § 1.98(a)(2)(i) for all U.S. national patent applications filed after June 30, 2003 and for all international applications that have entered the national stage under 35 USC § 371 after June 30, 2003. Also enclosed is a translation or a concise explanation of each non-English language document enclosed.

It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. § 1.56 and Section 609 of the MPEP. No fee is believed due. However, the Commissioner is hereby authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

Respectfully submitted,



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Traci A. Brown

<b>FORM PTO-1449</b> U.S. Department of Commerce Patent and Trademark Office  <b>LIST OF DOCUMENTS CITED BY APPLICANT</b>  (Use several sheets if necessary)					Attorney Docket No.: 5649-1265		Serial No.: To be assigned	
					Applicant: Seo et al.			
					Filing Date: Concurrently herewith		Group: Unknown	
<b>U. S. PATENT DOCUMENTS</b>								
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate	
	1	US2002/0072227 A1	6/13/02	Russell et al.	438	653		